

عنوان مقاله:

fast run-time FPGA fault location approach based on the column-based precompiled configuration techniques for
FPGA fault tolerance

محل انتشار:

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خلاصه مقاله:

run-time fault location in field-programmable gate arrays FPGAs is important because the resulting diagnostic information can be used to reconfigure the FPGA to tolerate permanent faults in order to minimize system downtime and increase availability a fault location technique with very short diagnostic latency is desired the abundance of configurable logic elements and routing resources in recent FPGAs provides a cost effective method for tolerating permanent fault occurs the FPGA can be reconfigured by replacing the faulty part with previously unused resources in the same hardware

کلمات کلیدی:

run-time ,FPGA,column-based , fault tolerance

لینک ثابت مقاله در پایگاه سیویلیکا:

<https://civilica.com/doc/128445>

