

عنوان مقاله:

Information Theory Based Analysis and Design of Sorting Networks

محل انتشار:

پانزدهمین کنفرانس مهندسی برق ایران (سال: 1386)

تعداد صفحات اصل مقاله: 6

نویسندگان:

Nemati - University of Tehran

Ghaderi - University of Tehran

خلاصه مقاله:

In this paper the concepts of information theory are utilized to perform the performance analysis of the sorting networks which is selected as an example of the parallel architectures. It is shown that using this method, the source of the redundancy and the shortcomings of the performance can be monitored specifically and an analytical proving for efficiency of designs can be presented. Also it is expected that the optimized design can be obtained at first try in design state instead of try and error methods. It may have more contribution in the large size and complicated approximate working architectures. In addition to this a more accurate boundaries for the performance characteristics can be determined using this method.

کلمات کلیدی:

Information Theory (IT), Sorting Networks, Parallel Architectures

لینک ثابت مقاله در پایگاه سیویلیکا:

<https://civilica.com/doc/25373>

