

عنوان مقاله:

A 9-bit 2-GSample/sN yquist Current-Steering CMOS DIA Converter

محل انتشار:

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خلاصه مقاله:

In this paper we present a high speed (2- Gsample/s) digital-to-analog converter suitable for using in an integrated circuit. To get the best DNL, monotonicity and reduce glitch energy, 100% segmentation has been used. A novel method is proposed or designed to reduce the error rate at the output of the digital circuit. We have used a modified latch and current source to improve transient switching behaviour of current-steering CMOS digital-to-analog converters (DACs). Power consumption is 214mW at Nyquist rate. The chip has been processed in a standard 0.35µm CMOS technology. The area of the chip is 1850µm² × 1070µm.

کلمات کلیدی:

Current-steering, DAC, Digital, Analog, Integrated circuits

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