

## عنوان مقاله:

A Combined Method to Improve Sequential Circuit Test Generation

## محل انتشار:

نهمین کنفرانس سالانه انجمن کامپیوتر ایران (سال: 1382)

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## خلاصه مقاله:

This paper proposes a combined method for sequential circuit test generation which employs STG-based and GA-based test generation techniques. Almost all previous hybrid test generators use algorithmic decomposition, but the proposed method uses circuit decomposition. So the STG-based techniques are used to generate test for control unit and test generation for datapath unit is performed by resorting to GA-based techniques. The combination of the two techniques is expected to provide high fault coverages in a reasonable time. Experimental results show the effectiveness of the approach.

## کلمات کلیدی:

لینک ثابت مقاله در پایگاه سیویلیکا:

<https://civilica.com/doc/45812>

