

عنوان مقاله:

A New Voltage Sag Simulator Topology for Testing Wind Turbines

محل انتشار:

اولین کنفرانس بین المللی دستاوردهای نوین پژوهشی در مهندسی برق و کامپیوتر (سال: 1395)

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خلاصه مقاله:

In this paper, a new semiconductor-based topology for a testing system of wind turbines is proposed, such that precise, fast and cost effective voltage reduction in accordance with standards of voltage sag test could be implemented. This device can be used as a tool to test the low voltage ride-through (LVRT) capability of wind turbines connected to the power grid. High voltage ride-through (HVRT) and asymmetrical voltage sag test capability are the other advantages of this system. The presented test system is simulated for doubly fed induction generator (DFIG) .wind turbine testing and the results are demonstrated

کلمات کلیدی:

component; Vvoltage sag; wind turbine test; grid codes; low voltage ride-through

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