عنوان مقاله:

Analysis of Different Verification Techniques for FPGA Design

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خلاصه مقاله:

there is growing interest in the market to implement FPGA-based systems. The escalating cost, time, and risk associated with custom integrated circuit (IC) fabrication has driven increased field programmable gate array (FPGA) usage across electronics applications. With the increasing size andcomplexity of FPGA devices, there is a need for more efficient verification methods. This paper is an effort to servey the different methodologies in te field of FPGA design verification

کلمات کلیدی:

FPGA, Verification, Simulation, prototyping,SOC

لینک ثابت مقاله در پایگاه سیویلیکا:

https://civilica.com/doc/658015