عنوان مقاله:

A Novel Seven-valued Buffer

محل انتشار:

سومین کنفرانس ملی فناوری در مهندسی برق و کامپیوتر (سال: 1397)

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خلاصه مقاله:

In many-valued logic, voltage regulation for individual logic levels plays an important role in building stacking capability for the circuitry. The main problem is the noise immunity and is shows itself in cascading a number of components such as full adders. Multi-Valued Buffer (MVB) represents a solution to improve noise immunity and to solve the cascading problem of multi-valued based circuits e.g. seven level full adder. The proposed MVB is designed in voltage mode. This buffer is expandable and it is capable of being implemented using more levels with the same approach.

کلمات کلیدی:

Multi-Valued Logic, Buffer, MVB, Noise Immunity

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